

Notice of References Cited

Application/Control No.

10/067,265

Applicant(s)/Patent Under
Reexamination
KIM ET AL.

Examiner

Asok K. Sarkar

Art Unit

2829

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,458,284	10-2002	Kashihara	216/47
	C	US-2002/0187434	12-2002	Blatchford et al.	430/322
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf and Tauber, "Silicon processing for the VLSI Era, Vol. 1", Chapter 16, p 539, Lattice Press, CA (1986).
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.